

L Number	Hits	Search Text	DB	Time stamp
27	334	712/200.ccls.	USPAT	2004/05/30 19:49
28	217	712/220.ccls.	USPAT	2004/05/30 19:49
29	21	circular with buffer with pointer with previous	USPAT; EPO; JPO	2004/05/30 19:49
30	20	"buffer allocation" same previous	USPAT; EPO; JPO	2004/05/30 19:49
31	119	"buffer allocation" same current\$2	USPAT; EPO; JPO	2004/05/30 19:49
32	19	"buffer allocation" same current\$2 same input	USPAT; EPO; JPO	2004/05/30 19:50
33	9	(round with robin) with allocation with buffer	USPAT; EPO; JPO	2004/05/30 19:50
34	53	(round with robin) same allocation same buffer	USPAT; EPO; JPO	2004/05/30 19:50
35	2	(round-robin) same allocation same availabl\$7 same buffer	USPAT; EPO; JPO; DERWENT	2004/05/30 19:50
36	58	(shift near3 register) with enable with (independent sepearate individual)	USPAT; EPO; JPO	2004/05/30 19:50
37	64	(circular with buffer) same allocat\$5 same available	USPAT; EPO; JPO; DERWENT	2004/05/30 19:50
-	2199	buffer with allocation	USPAT	2003/09/21 20:29
-	1	buffer with allocation with cascade	USPAT	2003/09/08 00:17
-	2	(buffer with allocation) same cascade	USPAT	2003/09/08 00:17
-	5	(buffer with allocation) same cascad\$4	USPAT	2003/09/08 00:17
-	629	"buffer allocation"	USPAT	2003/09/08 12:23
-	67	"buffer allocation" same input same output	USPAT	2003/09/11 13:20
-	0	"buffer allocation" same "AND" same "or"	USPAT	2003/09/08 12:29
-	7	"buffer allocation" same input same output same available	USPAT	2003/09/08 12:29
-	2	4510581.URPN.	USPAT	2003/09/09 17:11
-	2303	cascad\$4 same (available enable) same output	USPAT	2003/09/10 10:45
-	1125	cascad\$4 same (available enable) same output same circuit	USPAT	2003/09/10 10:46
-	850	cascad\$4 same (available enable) same output same circuit same input	USPAT	2003/09/10 10:50
-	229	cascad\$4 same (available enable) same output same circuit same input same count\$4	USPAT	2003/09/10 12:17
-	197	cascad\$4 same (available enable) same output same circuit same input same select\$3	USPAT	2003/09/10 12:17
-	26	cascad\$4 same (available enable) same output same circuit same input same increment\$2	USPAT	2003/09/10 12:20
-	2	cascad\$4 same iterative same increment\$2	USPAT	2003/09/10 14:00
-	4867	(shift near3 register) with enable	USPAT	2003/09/10 14:01
-	54	(shift near3 register) with enable with (independent sepearate individual)	USPAT	2004/05/30 19:50
-	250	(circular with buffer) same allocat\$5	USPAT	2003/09/11 11:51
-	59	(circular with buffer) same allocat\$5 same available	USPAT	2004/05/30 19:50
-	2741	interleaved with memory	USPAT	2003/09/11 11:59

-	6	(interleaved with memory) same allocat\$5 same available	USPAT	2003/09/11 12:00
-	18	"buffer allocation" same previous	USPAT	2004/05/30 19:49
-	111	"buffer allocation" same current\$2	USPAT	2004/05/30 19:49
-	29	"buffer allocation" same current\$2	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 13:22
-	6	"buffer allocation" same previous	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 13:23
-	29	"buffer allocation" same current\$2	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 13:23
-	19	"buffer allocation" same current\$2 same input	USPAT	2004/05/30 19:50
-	5	"buffer allocation" same current\$2 same input	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 15:05
-	0	"buffer allocation" same (current\$2 with select\$4)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 15:06
-	3	"buffer allocation" same (current\$2 with select\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 15:06
-	2	"buffer allocation" same (current\$2 with input)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/11 15:06
-	454	(available with vector) same buffer	USPAT; US-PGPUB	2003/09/15 12:40
-	23	(available with vector) same buffer same allocat\$4	USPAT; US-PGPUB	2003/09/15 15:14
-	5	(pickup start begin) with (left-off leftoff)	USPAT; US-PGPUB	2003/09/15 15:16
-	4	(pickup start begin) same startover	USPAT; US-PGPUB	2003/09/15 15:21
-	21055	(pickup start begin) with last	USPAT; US-PGPUB	2003/09/15 15:21
-	1803	(pickup start begin) with last with (memory buffer)	USPAT; US-PGPUB	2003/09/15 15:24
-	77	(pickup start begin) with last with (memory buffer) with availabl\$4	USPAT; US-PGPUB	2003/09/15 15:24
-	956	((begin start) with last) same delay	USPAT	2003/09/16 10:18
-	46	((begin start) with last with device) same delay	USPAT	2003/09/16 10:18
-	28	(inverter with "and gate") same allocation	USPAT	2003/09/21 20:29
-	2	(inverter with "and-gate" with "or-gate") same allocation	USPAT	2003/09/21 20:30
-	12	(inverter with "and gate" with "or gate") same allocation	USPAT	2003/09/21 20:32
-	1	(inverter with "and gate" with "or gate") same allocation same availabl\$7	USPAT	2003/09/21 20:32
-	1	(inverter with "and gate" with "or gate") same allocation same availabl\$7	USPAT	2003/09/21 20:33
-	21	(round-robin) same allocation same availabl\$7	USPAT	2003/09/21 20:34
-	41	(round with robin) same allocation same availabl\$7	USPAT	2003/09/21 22:22
-	2	(round with robin) same allocation same buffer same availabl\$7	USPAT	2003/09/21 20:34

-	2	(round-robin) same allocation same availabl\$7 same buffer	USPAT	2004/05/30 19:50
-	44	(round with robin) same allocation same buffer	USPAT	2004/05/30 19:50
-	8	(round with robin) with allocation with buffer	USPAT	2004/05/30 19:50
-	38	circular with allocation with buffer	USPAT	2003/09/21 22:50
-	108	previous same (allocation with buffer)	USPAT	2003/09/21 22:43
-	5	cascad\$4 same (allocation with buffer)	USPAT	2003/09/21 22:41
-	24	previous same (allocation with buffer) same input	USPAT	2003/09/21 22:43
-	10	(previous with input) same (allocation with buffer)	USPAT	2003/09/21 22:43
-	626	circular with buffer with pointer	USPAT	2003/09/21 22:50
-	15	circular with buffer with pointer with allocat\$6	USPAT	2003/09/21 22:51
-	16	circular with buffer with pointer with previous	USPAT	2004/05/30 19:49